A Partnership Including Professional Corporations 600 13th Street, N.W. Washington, D.C. 20005-3096 (202) 756-8000

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# MCDERMOTT, WILL & EMERY

### FACSIMILE

<b>FROM:</b> Attorney: Attorney's E-Mail: Secretary:		G D D 1	Dina at Dhanas	202-756-8016		
		Scott D. Paul	Direct Phone:			
		Spaul@mwe.com				
		Connie Collins	Direct Phone:	202-756-8659		
Client/Matter/Tkpr: 57454-062		57454-062	Date: 11/4/02	Time Sent:		
			Number o	-17-		
<u>TO:</u>						
Name:	Examiner Johannes Mondt		Facsimile No.	703-746-5176		
Company:	Company: U.S. Patent & Trademark Office		Contact No.	703-306-0531		
			<del></del>			

MESSAGE: Per our telephone conference, attached is the timely filing of Sept 4, 2002.

PLEASE <u>CONFIRM RECEIPT</u> OF THIS FAX BY PHONING CONSTANCE COLLINS AT THE ABOVE NUMBER. THANK-YOU

The information contained in this facsimile message is legally privileged and confidential information intended only for the use of the individual or entity named above. If the reader of this message is not the intended recipient, you are hereby notified that any dissemination, distribution, or copy of this facsimile is strictly prohibited. If you have received this facsimile in error, please notify us immediately by telephone and return the original message to us at the above address via the United States Postal Service. Thank you.wdc99 453298-11.049657.0677

# FAX RECEIVED

NOV 4 2002

T.C. 2800

Docket .: 57454-062

**PATENT** 

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: RESPONSE UNDER 37 CFR 1.116

- EXPEDITED PROCEDURE

Tomohide TERASHIMA

Serial No.: 09/834,954

Group Art Unit: 2826

Filed: April 16, 2001

Examiner: Johannes P. Mondt

For:

**SEMICONDUCTOR DEVICE** 

**BOX AF** 

THE COMMISSIONER FOR PATENTS AND TRADEMARKS Washington, DC 20231

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	12	20	0	\$18.00 =	\$0.00
Independent Claims	2	3	0	\$84.00 =	\$0.00
		Multiple claims newly presented			\$0.00
		Fee for extension of time			\$0.00
					\$0.00
Total of Above Calculations				\$0.00	

Please charge my Deposit Account No. <u>500417</u> in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Scott D. Paul

Registration No. 42,984

600 13th Street, N.W. Washington, DC 20005-3096 (202)756-8000 SDP:cac Facsimile: (202)756-8087 Date: September 4, 2002

N.B. Please enter at this time Thank your of Please: Do Not enter. They

Docket No.: 57454-062

(12/18/02)

**PATENT** 

(NE)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: RESPONSE UNDER 37 CFR 1.116

**EXPEDITED PROCEDURE** 

Tomohide TERASHIMA

Application No.: 09/834,954

Group Art Unit: 2826

Filed: April 16, 2001

Examiner: J. Mondt

For: SEMICONDUCTOR DEVICE

## **AMENDMENT UNDER 37 C.F.R. § 1.116**

#### BOX AF

The Commissioner for Patents and Trademarks Washington, DC 20231

Sir:

The following Amendment and Remarks are submitted in response to the Final Office Action dated June 4, 2002, pursuant to the provisions of 37 C.F.R. § 1.116. Please amend the above-identified application as follows:

#### IN THE CLAIMS:

Please amend claims 1 and 10 as follows:

- 1. (Amended) A semiconductor device including:
- a semiconductor substrate having a main surface;
- a semiconductor layer of a first conductive type which is formed on the main surface of said semiconductor substrate;
- a first buried impurity region of the first conductive type formed between said semiconductor layer and said semiconductor substrate;